



060.68726

PATENT APPLICATION

JFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Yoshihiro Kizaki
Serial No.: 10/706,664
Conf. No.: 8620
Filed: 11/12/2003
For: SEMICONDUCTOR INTEGRATED CIRCUIT
Art Unit: 2816
Examiner: Zweizig, Jeffery Shawn

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

11/19/05

Date

Registration No. 41,760
Attorney for Applicant(s)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT TRANSMITTAL

Dear Sir:

Transmitted herewith is a communication regarding the above-identified application.

Fee Calculation For Claims As Amended

	As Amended	Previously Paid For	Present Extra	Rate	Additional Fee
Total Claims	8	- 28 =	0	x \$ 50.00	= \$ 0
Independent Claims	2	- 4 =	0	x \$200.00	= \$ 0
Fee for Multiple Dependent Claims				\$360.00	= \$ 0
		Total Additional Fee			\$ 0
		Small Entity Fee (reduced by half)			\$ 0

(X) Amendment A.

(X) If a Petition under 37 C.F.R. §1.136(a) for an extension of time for response is required to make the attached response timely and does not separately accompany this transmittal, Applicant(s) hereby petition(s) under 37 C.F.R. §1.136(a) for an extension of time for response in the above-identified application for the period required to make the attached response timely.

(X) The Commissioner is hereby authorized to charge any additional fees which may be required to this application under 37 C.F.R. §§1.16-1.17, or credit any overpayment, to Deposit Account No. 07-2069. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

April 19, 2005
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GREER, BURNS & CRAIN, LTD.

By:
Joseph P. Fox, Reg. No. 41,760